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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/708,381	02/27/2004	Xiangdong Chen	FIS920030367US1	2380	
23389 759	90 11/28/2005		EXAM	INER	
SCULLY SCOTT MURPHY & PRESSER, PC 400 GARDEN CITY PLAZA			SOFOCLEOUS,	SOFOCLEOUS, ALEXANDER	
SUITE 300	CITY PLAZA		ART UNIT	PAPER NUMBER	
GARDEN CITY, NY 11530			2824		
			DATE MAILED: 11/28/2009	5	

Please find below and/or attached an Office communication concerning this application or proceeding.

•	Application No.	Applicant(s)				
	10/708,381	CHEN ET AL.				
Office Action Summary	Examiner	Art Unit				
	Alexander Sofocleous	2824				
The MAILING DATE of this communication ap	pears on the cover sheet with the	correspondence address				
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATIO 136(a). In no event, however, may a reply be till will apply and will expire SIX (6) MONTHS from te, cause the application to become ABANDONE	N. mely filed n the mailing date of this communication. ED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on	-					
	 s action is non-final.					
3) Since this application is in condition for allowa		osecution as to the marits is				
closed in accordance with the practice under						
:	Expans quayio, 1000 0.B. 11, 1	33 3.3. 210.				
Disposition of Claims						
4) Claim(s) 1-20 is/are pending in the application	n.					
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.		0_11				
6) Claim(s) is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) <u>1-20</u> are subject to restriction and/or	election requirement.					
Application Papers						
9) The specification is objected to by the Examin	er					
10)☐ The drawing(s) filed on is/are: a)☐ ac		Examiner.				
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correct	• • • • • • • • • • • • • • • • • • • •					
11) The oath or declaration is objected to by the E	•					
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreiga) All b) Some * c) None of:	n priority under 35 U.S.C. § 119(a	n)-(d) or (f).				
 Certified copies of the priority document 	its have been received.					
2. Certified copies of the priority documen	its have been received in Applicat	tion No				
Copies of the certified copies of the price	ority documents have been receiv	ed in this National Stage				
application from the International Burea	au (PCT Rule 17.2(a)).					
* See the attached detailed Office action for a lis	t of the certified copies not receive	ed.				
		•				
		· (
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) 🔀 Interview Summan	y (PTO-413)				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail D					
 Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 	6) Other:	ratent Application (PTO-152)				

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DETAILED ACTION

- 1. This action is responsive to the following communications: the Application filed on February 27, 2004, and the Information Disclosure Statement filed on June 25, 2004.
- 2. Claims 1-20 are pending in the case. Claim 1, 9, 15, and 19 are independent claims.

Election/Restrictions

- 3. Restriction to one of the following inventions is required under 35 U.S.C.121:
- Group I. Claims 1-8, and 15-18, drawn to DRAM with a trench capacitor, classified in class 365, subclass 149. (Systems using particular element → Capacitors)
- Group II. Claims 9-14, drawn to method of fabricating a DRAM with a trench capacitor, classified in class 438, subclass 259. (Making field effect device having pair of active regions separated by gate structure by formation or alteration of semiconductor active regions → Having junction gate, e.g., JFET, SIT → Having additional gate electrode surrounded by dielectric, i.e., floating gate → Including forming gate electrode in trench or recess in substrate)
- Group III. Claims 19-20, drawn to a vertical transistor, classified in class 257, subclass 220. (Field effect device → Charge transfer device → Majority signal

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carrier, e.g., buried or bulk channel, or peristaltic → Impurity concentration variation → Vertically within channel, e.g., profiled)

4. The inventions are distinct, each from the other because of the following reasons:

Inventions Group I and Group II are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case Group I does not require that the deep trench be filled with polysilicon.

Inventions Group I and Group III are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention Group III has separate utility such as not requiring a word-line voltage. See MPEP § 806.05(d).

5. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

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6. A telephone call was made to Steven Fischman on 11/22/2005 to request an oral election to the above restriction requirement, but did not result in an election being made.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Conclusion

When responding to this office action, applicants are advised to provide the examiner with the line numbers and page numbers in the application and/or references cited to assist the examiner in locating appropriate paragraphs.

A shortened statutory period for response to this action is set to expire three months and zero days from the date of this letter. Failure to respond within the period for response will cause this application to become abandoned (see MPEP 710.02(b)).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alexander Sofocleous whose telephone number is 571-272-0635. The examiner can normally be reached on 7:00am - 4pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on 571-272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AGS

VANTHUNGUYEN PRIMARY EXAMINER

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